



PIN	SYMBOL	I/O	FUNCTION		
1	VSS	I	Ground pin, connected to 0V		
2	VDD	I	Power supply pin for logic .(+5V)		
3	V0	I	Contrast control (-11.0V)		
4	/WR	I	Data write: Write data into T6963 when /WR is L.		
5	/RD	I	Data Read: Read data from T6963 when /RD is L.		
6	/CE	I	Chip enable for T6963: /CE must be L when CPU communicates with T6963		
7	C/D	I	C/D	/WR=L	/RD=L
			L	Data Write	Data Read
			H	Command Write	Status Read
8	RESET	I	Reset signal: H: Normal (T6963 has internal pull-up resistor) L: Initialize T6963. Text and graphic have address and text and graphic area setting are retained.		
9	DB0	I/O	Data I/O pins for display memory.		
10	DB1	I/O	Data I/O pins for display memory.		
11	DB2	I/O	Data I/O pins for display memory.		
12	DB3	I/O	Data I/O pins for display memory.		
13	DB4	I/O	Data I/O pins for display memory.		
14	DB5	I/O	Data I/O pins for display memory.		
15	DB6	I/O	Data I/O pins for display memory.		
16	DB7	I/O	Data I/O pins for display memory.		
17	FS	I	Pin for selection of font: FS=H: 6x8 dots FS=L: 8x8 dots		
18	NC	-	No connect		
19	LEDK	-	LED cathode (0V)		
20	LEDA	-	LED anode (+5V)		

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ISO9001 - CN17/30422
ISO14001 - CN17/30423
IATF16949 - CN17/30525