



PIN	SYMBOL	I/O	FUNCTION
1	VSS	-	Ground pin, connected to 0V
2	VDD	-	Power for supply(5.0V)
3	VO	I	Contrast control (VDD~VOOUT)
4	RS	I	Data or Instruction: RS=H : DB0~Db7 Display RAM Data. RS=L : DB0~Db7 Instruction Data.
5	R/W	I	Read or Write: R/W=H : Data appears at DB0~DB7 and can be read by the CPU while E =H. R/W=L :Display data DB0~DB7 can be written at falling of E.
6	E	I	Enable signal: Write mode(R/W=L): data of DB0~DB7 is latched at the falling edge of E. Read mode(R/W=H): DB0~DB 7 appears the reading data while E is at high level.
7	DB0	I/O	Data bus, three state I/O common terminal.
8	DB1	I/O	Data bus, three state I/O common terminal.
9	DB2	I/O	Data bus, three state I/O common terminal.
10	DB3	I/O	Data bus, three state I/O common terminal.
11	DB4	I/O	Data bus, three state I/O common terminal.
12	DB5	I/O	Data bus, three state I/O common terminal.
13	DB6	I/O	Data bus, three state I/O common terminal.
14	DB7	I/O	Data bus, three state I/O common terminal.
15	CS1	I/O	Chip selection: CS1=H : Left screen selected.
16	CS2	I/O	Chip selection: CS1=H : Right screen selected.
17	/RES	I/O	Reset signal: /RES= L : Chip reset,
18	VEE	I/O	Power output , LCD power supply(-8.8V output)
19	LEDA	I/O	LED anode (+5V)
20	LEDK	I/O	LED cathode (0V)

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