



PIN NO.	Symbol	Level	Function
1	VDD	+5.0V	Power supply for Logic
2	VSS	0	Ground
3	VLCD	0~+ 5V	Power supply for LCD Driver
4	/RET	H/L	Reset signal. Low -Active
5	E 1	H/L	Read/Write enable signal (MASTER)
6	E 2	H/L	Read/Write enable signal (SLAVE)
7	R/W	H/L	Read/Write select signal R/W=H: Read; R/W=L: WRITE.
8	RS	H/L	Register selection signal RS=H: Data register RS=L: Instruction register
9	DB 0	H/L	Data bit 0
10	DB 1	H/L	Data bit 1
11	DB 2	H/L	Data bit 2
12	DB 3	H/L	Data bit 3
13	DB 4	H/L	Data bit 4
14	DB 5	H/L	Data bit 5
15	DB 6	H/L	Data bit 6
16	DB 7	H/L	Data bit 7
17	NC	--	No connect
18	NC	--	No connect

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