



Unmarked tolerance: $x.xx \pm 0.20$
 $x.x \pm 0.3$

WHIT EL B/L
WITHOUT B/L

PIN	SYMBOL	I/O	FUNCTION
1	VSS	-	Ground pin, connected to 0V
2	VDD	-	Power supply pin for logic (+5V)
3	V0	-	Contrast control (VDD~VSS)
4	RS	I	A signal for selecting registers: 1: Data register (for read and write) 0: Instruction register (for write) Busy flag – Address Counter (for read).
5	R/W	I	A signal for selecting read and write actions: 1: Read 0: Write
6	E	I	A signal for selecting reading and writing data: 1: Start 0: Stop
7	DB0	I	Tristate input/output pins. Connect these pins to an 8-bit microprocessor bus.
	DB1	I	Tristate input/output pins. Connect these pins to an 8-bit microprocessor bus.
9	DB2	I/O	Tristate input/output pins. Connect these pins to an 8-bit microprocessor bus.
10	DB3	I/O	Tristate input/output pins. Connect these pins to an 8-bit microprocessor bus.
11	DB4	I/O	Tristate input/output pins. Connect these pins to an 4-bit/8-bit microprocessor bus.
12	DB5	I/O	Tristate input/output pins. Connect these pins to an 4-bit/8-bit microprocessor bus.
13	DB6	I/O	Tristate input/output pins. Connect these pins to an 4-bit/8-bit microprocessor bus.
14	DB7	I/O	Tristate input/output pins. Connect these pins to an 4-bit/8-bit microprocessor bus.
15	EL	-	EL backlight pin
16	EL	-	EL backlight pin