



PIN NO.	Symbol	Level	Function
1	DB 0	H/L	Data bus 0
2	DB 1	H/L	Data bus 1
3	DB 2	H/L	Data bus 2
4	DB 3	H/L	Data bus 3
5	DB 4	H/L	Data bus 4
6	DB 5	H/L	Data bus 5
7	DB 6	H/L	Data bus 6
8	DB7	H/L	Data bus 7
9	E	H/L	Enable signal for LCM for chip 1
10	R/W	H/L	Read/Write selection(H:Read,L:Write)
11	RS	H/L	Register selection(H:Data Register,L:Instruction Register)
12	V 0	---	Power supply for LCD driver
13	VSS	0V	Ground for Logic
14	VDD	+ 5.0 V	Power supply for Logic
15	E 2	H/L	Enable signal for LCM for chip 2
16	VEE	---	DC -DC output voltage
17	A	---	Backlight(+ 5.0 V)
18	K	---	Backlight(0V)