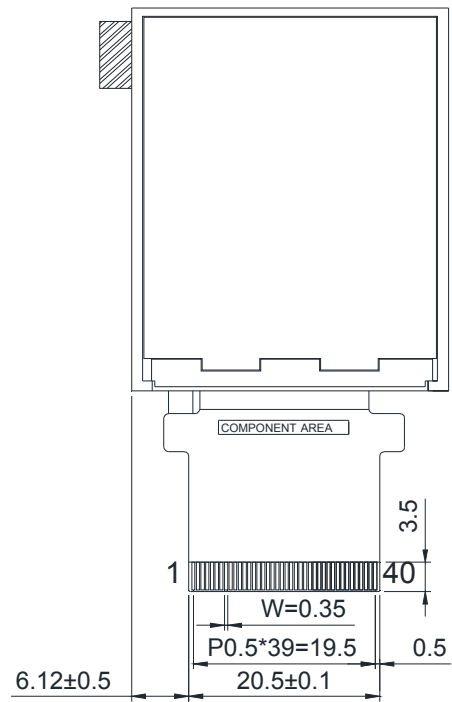


IM2	IM1	IM0	MPU Interface	GRAM
1	0	1	8080 MCU 16-bit bus	D[15:0]
1	0	0	8080 MCU 8-bit bus	D[7:0]
0	—	—	3-Line SPI	(D0 : SDA) (D/C : SCL) (SPI_CS)



1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
VL+	VL+	IM0	IM1	IM2	RST	NC	NC	NC	NC	NC	NC	DB15	DB14	DB13	DB12	DB11	DB10	DB9	DB8
21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0-SPI-SCL	NC	RD	WR	D/C-SPI-SCL	CS	VCC	VCI	GND	NC	NC	NC	NC

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